

INFORMATION DISCLOSURE
CITATION

ATTY. DOCKET NO.

SERIAL NO.

2380-788

10/629,773

APPLICANT

HÖRLIN et al

FILING DATE

GROUP

July 30, 2003

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
<i>RD</i>	5,721,833	2-1998	Cooperman et al.			
	5,862,128	1-1999	Cooperman et al.			
	5,487,061	1-1996	Bray			
	5,499,238	3-1996	Shon			
	5,757,771	5-1998	Li et al.			
	5,914,934	6-1999	Rathnavelu			
	5,493,566	2-1996	Ljungberg et al.			
	5,550,823	8-1996	Irie et al.			
	5,619,500	4-1997	Hiekali			
	5,768,257	6-1998	Khacherian et al.			
	5,790,522	8-1998	Fichou et al.			
	5,828,653	10-1998	Goss			
	5,537,400	7-1996	Diaz et al.			
	6,480,492	11-2002	Lundback et al.			
	6,128,295	10-2000	Larsson et al.			
	6,034,958	3-2000	Wicklund			
<i>RD</i>	6,483,831	11-2002	Petersen			

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FOREIGN PATENT DOCUMENTS

DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
00/38380	6-2000	WO			

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent pages, etc.)

<i>RD</i>	Mirfakhraei, "Wafer-Scale Integration as a Technology Choice for High Speed ATM Switching Systems", Proceedings of the Annual International Conference on Wafer Scale Integration (from 1996 Proceedings of the Int'l. Conference on Innovative Systems in Silicon), US, New York, IEEE, vol. CONF. 6, 1994, pp. 282-291
<i>RD</i>	Doi et al, "A High-Speed ATM Switch Architecture for FTTH - An ATM Switch Architecture iwth Input and Cross-Point Buffers", Proceedings of the International Switching Symposium, DE, Berlin, VDE Verlag, vol. SYMP. 15, 23 April 1995, pp. 384-388

*Examiner	<i>Heide</i>	Date Considered	30 Sept 04
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Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application.

Form PTO-FB-A820 (Also PTO-1449)